

Table 4.13  
ERROR CODE DEFINITIONS

ERROR CODE	WHEN DETECTED				CAUSE OF ERROR	ACTION TO CLEAR ALARM
	LOAD MODE	PROG. CHECK	SWITCH TO RUN	POWER UP		
E 01	0				Incorrect entry of instruction or data.	CLR (Push CLR key)
E 02		0			Instruction and I/O data wrong. Input programmed as an Output.	CLR Change reference
E 03		0			Instructions that can be stacked (MCS, MCR, etc.) exceed 8 levels.	CLR
E 05		0			Output coil, internal relay, timer or Counter reference already used.	CLR Use new reference
E 06		0			Number of MCR instructions greater than number of MCS instructions.	CLR
E 07		0			Timer, Counter or Shift Register required condition incomplete.	CLR Check program
E 08		0	0		No Timer or Counter preset value	CLR
E 09		0			Incomplete rung	CLR Check program entered
E 10	0				Two-word instruction written to last program memory address (4095), no room for second word.	CLR
E 11		0			All program memory locations used.	CLR
E 13	●	●	●	●	Momentary power failure.	CLR
E 21		●	●	●	Program memory parity error	Switch to PROG, push CLR
E 22			●	●	Lithium back-up battery <2.7 V dc.	CLR Change battery
E 25	0				Contents of cassette tape and PC memory not equal.	CLR and Clear relay 7066 with forced RST
E 28	0				Improper level of recorder volume control.	CLR Adjust volume control to about mid-range
E 31	●	●	●	●	Watch-dog timer >300 msec (timed out).	CLR Switch to PROG, push CLR
E 41				0	I/O module configuration change since last power-up.	Switch to RUN 1, push SET then CLR (Updates I/O map)
E 50	0				Invalid I/O → CPU transfer	Switch to RUN 1 → RST → CLR
E 50	0				Invalid data to or from peripheral device.	CLR, then clear relay 7077 with forced RST.
E 75	0				Contents of PROM and CMOS RAM are not equal.	CLR
E 76	0				PROM to CMOS RAM data transfer invalid or defective CMOS RAM.	CLR
E 99	0				Instruction or data not found when a SEARCH operation is initiated.	CLR

1. 0 indicates an improper programming operation.
2. ● indicates a system problem.